

Attorney Docket No.: 1384-1023

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application: John L. Pierce

Serial No.: 09/738,193

FAX RECEIVED

Filed: December 15, 2000

MAR 19 2003

Art Unit: 2827

TECHNOLOGY CENTER 2800

Examiner: David E. Graybill

For: System, Method and Apparatus for Constructing
a Semiconductor Wafer-Interposer Using B-
Stage Laminates

Commissioner of Patents
Washington, D.C. 20231

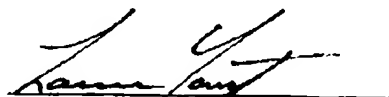
Petition Under 37 C.F.R. §1.136

Dear Sir:

Applicant hereby respectfully requests a two month extension of time pursuant to 37 C.F.R. 1.136 for filing a reply. The prescribed fee set forth in 37 C.F.R. 1.17(a)(2) of \$205.00 is enclosed herewith.

Dated this 19th day of March, 2003.

Respectfully submitted:



Lawrence R. Youst
Reg. No. 38,795
Attorney for Applicant and Assignee